

FIG I      FIG II

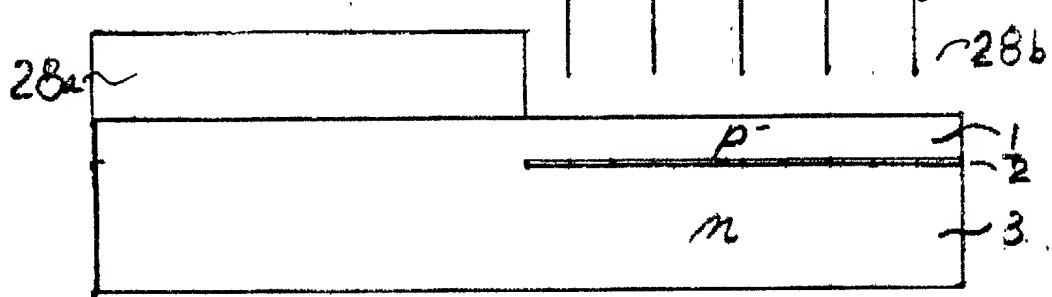
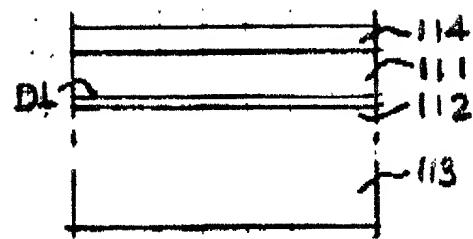


FIG 3

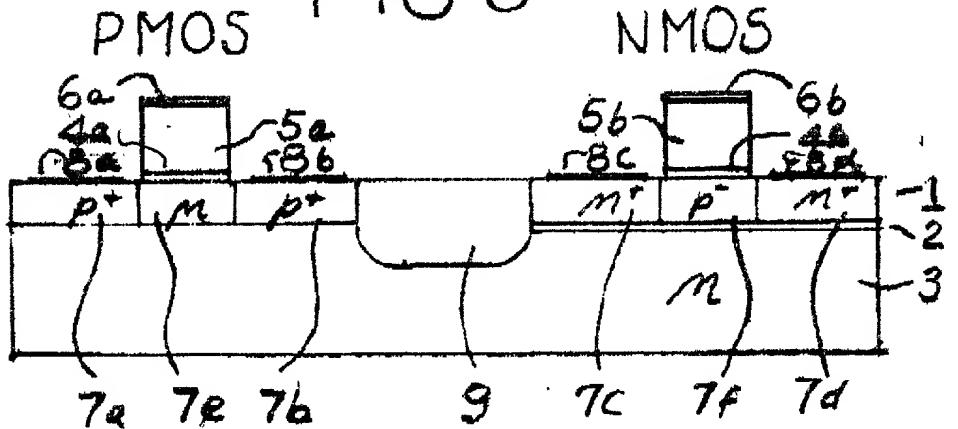


FIG 5

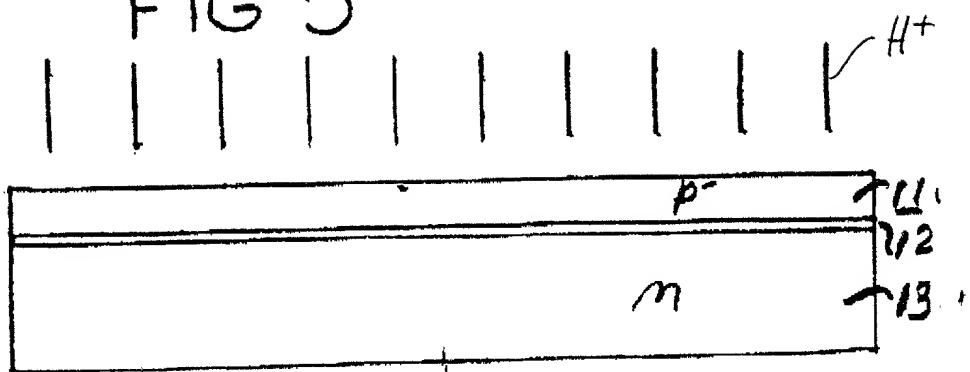


FIG 6

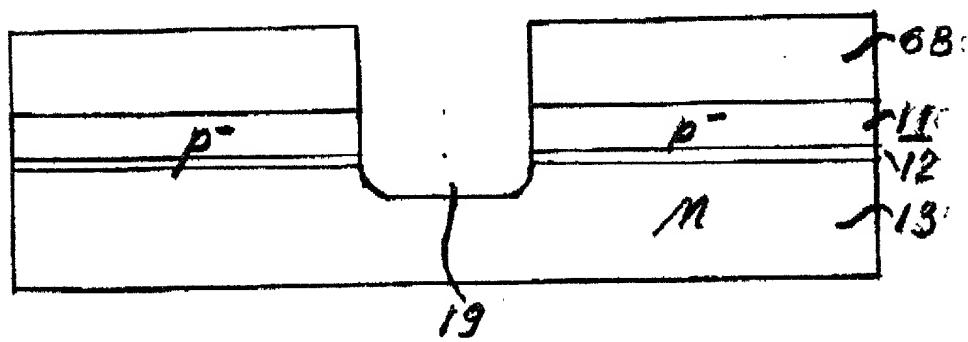
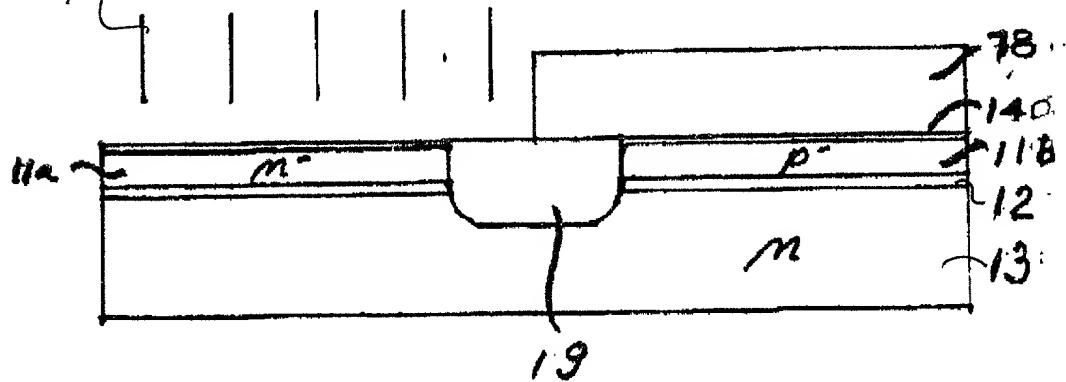
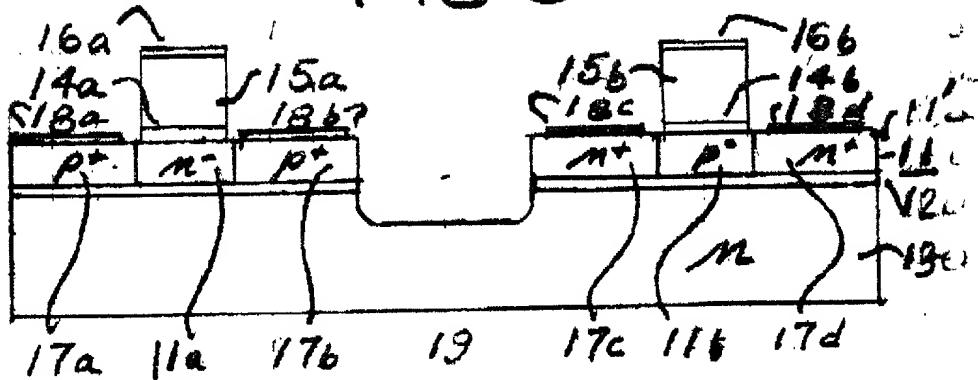


FIG 7



PMOS FIG 8 NMOS



PMOS FIG 9 NMOS

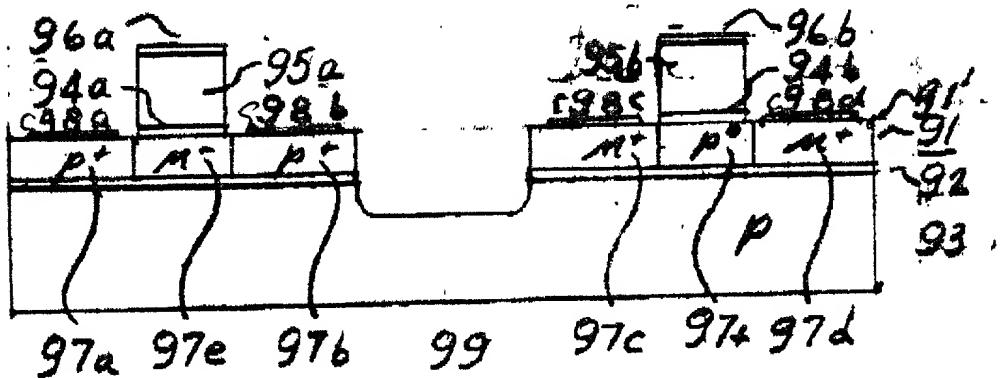


FIG 4

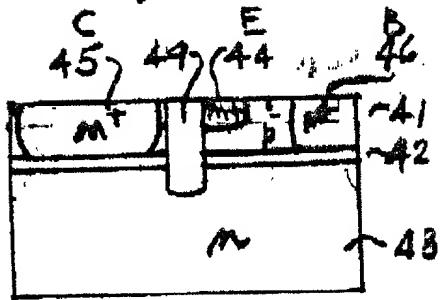


FIG 10

